

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: H. MIZUNO et al
Serial No. 10/319,511
Filed: December 16, 2002

Group Art Unit: 2818
Examiner: T. Le
For: SEMICONDUCTOR DEVICE

TRANSMITTAL OF CORRECTED FORMAL DRAWINGS

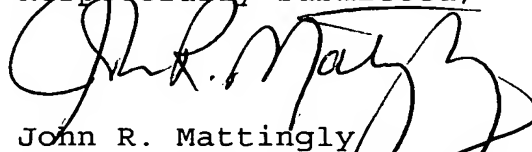
Commissioner of Patents
Alexandria, VA 22314

Sir:

Applicants submit herewith one (1) sheet of corrected formal drawings (Fig. 37). Please substitute the corrected sheet for the originally-filed sheet of drawings. None of the other sheets of drawings require correction.

Please charge any additional fees resulting from this action to Deposit Account No. 50-1417.

Respectfully submitted,

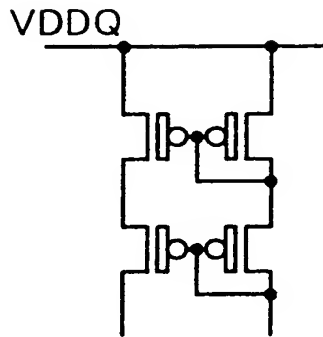
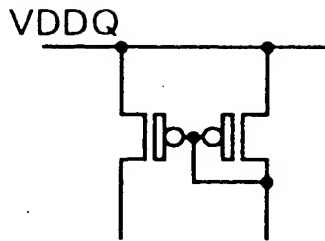


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Date: January 15, 2004

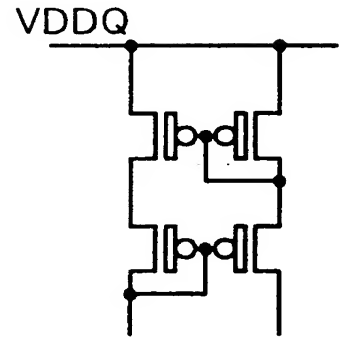
FIG.37

FIG. 37 (a)



Cascoded
current source

FIG. 37 (b)



Wilson
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